

LS XEC Cnet

Supported Series: LS XGB Series XEC CPU with communication module XGL-CH2A

Website: <http://www.lgis.com/>

HMI Setting:

Parameters	Recommended	Options	Notes
PLC type	LS XEC Cnet		
PLC I/F	RS232	RS232/RS485	
Baud rate	115200	9600 ~ 115200	
Data bits	8	7, 8	
Parity	None	Even, Odd, None	
Stop bits	1	1,2	
PLC sta. no.	0	0 ~ 32	

Device Address:

Bit/Word	Device type	Format	Range	Memo
B	AW_Bit	DDDDDDh	0 ~ 262143f	Automatic variable bit
B	IW_Bit	DDD.DD.Dh	0 ~ 127.15.3f	Input device bit
B	QW_Bit	DDD.DD.Dh	0 ~ 127.15.3f	Output device bit
B	MW_Bit	DDDDDDh	0 ~ 131071f	Direct variable bit
B	RW_Bit	DDDDDDh	0 ~ 32767f	Direct variable bit
B	WW_Bit	DDDDDDh	0 ~ 65535f	Direct variable bit
B	FW_Bit	DDDDh	0 ~ 2047f	System flag bit
B	KW_Bit	DDDDh	0 ~ 8399f	Built-in special flag bit
B	LW_Bit	DDDDDDh	0 ~ 11263f	High speed link flag bit
B	NW_Bit	DDDDDDh	0 ~ 25087f	P2P flag bit
B	UW_Bit	DD.DD.DDh	0 ~ 31.15.31f	Analog flag bit
B	AX	DDDDDDDD	0 ~ 4194303	
B	IX	DDD.DD.DD	0 ~ 127.15.63	
B	QX	DDD.DD.DD	0 ~ 127.15.63	
B	MX	DDDDDDDD	0 ~ 2097151	
B	RX	DDDDDD	0 ~ 524287	
B	WX	DDDDDDDD	0 ~ 1048575	
B	FX	DDDDDD	0 ~ 32767	
B	KX	DDDDDD	0 ~ 134399	

Bit/Word	Device type	Format	Range	Memo
B	LX	DDDDDDDD	0 ~ 1880223	
B	NX	DDDDDD	0 ~ 401407	
B	UX	DD.DD.DDD	0 ~ 31.15.511	
W	AW	DDDDDD	0 ~ 262143	Automatic variable
W	IW	DDD.DD.D	0 ~ 127.15.3	Input device
W	QW	DDD.DD.D	0 ~ 127.15.3	Output device
W	MW	DDDDDD	0 ~ 131071	Direct variable
W	RW	DDDDD	0 ~ 32767	Direct variable
W	WW	DDDDD	0 ~ 65535	Direct variable
W	FW	DDDD	0 ~ 2047	System flag
W	KW	DDDD	0 ~ 8399	Built-in special flag
W	LW	DDDDD	0 ~ 11263	High speed link flag
W	NW	DDDDD	0 ~ 25087	P2P flag
W	UW	DD.DD.DD	0 ~ 31.15.31	Analog flag
DW	MD	DDDDD	0 ~ 65535	

Wiring Diagram:

RS232 Terminal (Diagram 1 ~ Diagram 3)

Diagram 1

cMT Series	<i>cMT3071 / cMT3072 / cMT3090 / cMT3103 / cMT3151</i>
eMT Series	<i>eMT3070 / eMT3105 / eMT3120 / eMT3150</i>
MT-iE	<i>MT8073iE / MT8102iE</i>
MT-XE	<i>MT8092XE</i>
MT-iP	<i>MT6103iP / MT8102iP</i>

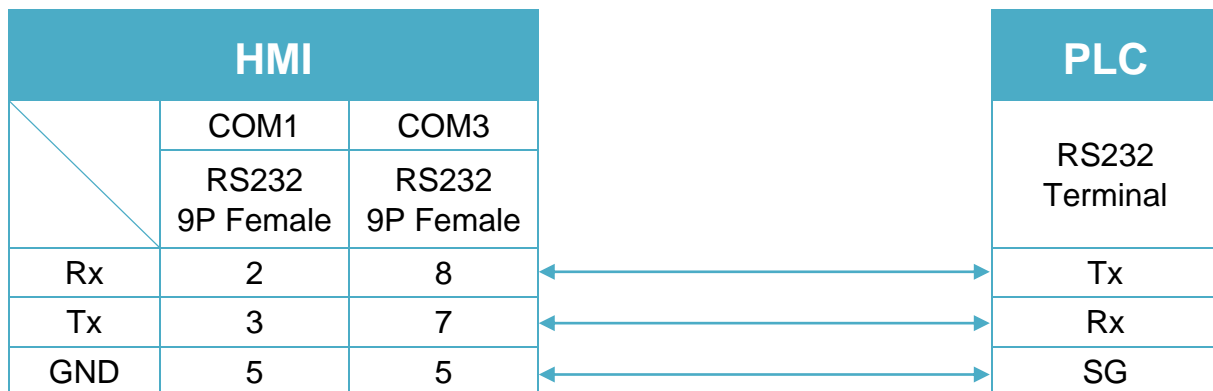


Diagram 2

cMT Series	<i>cMT-SVR / cMT-G01 / cMT-G02 / cMT-HDM / cMT-FHD</i>
mTV	<i>mTV</i>
MT-iE	<i>MT8070iE / MT6070iE / MT8100iE / MT8121iE / MT8150iE / MT8071iE / MT6071iE / MT8072iE / MT6072iE / MT8073iE / MT8101iE / MT8102iE / MT8103iE</i>
MT-XE	<i>MT8121XE / MT8150XE / MT8090XE</i>

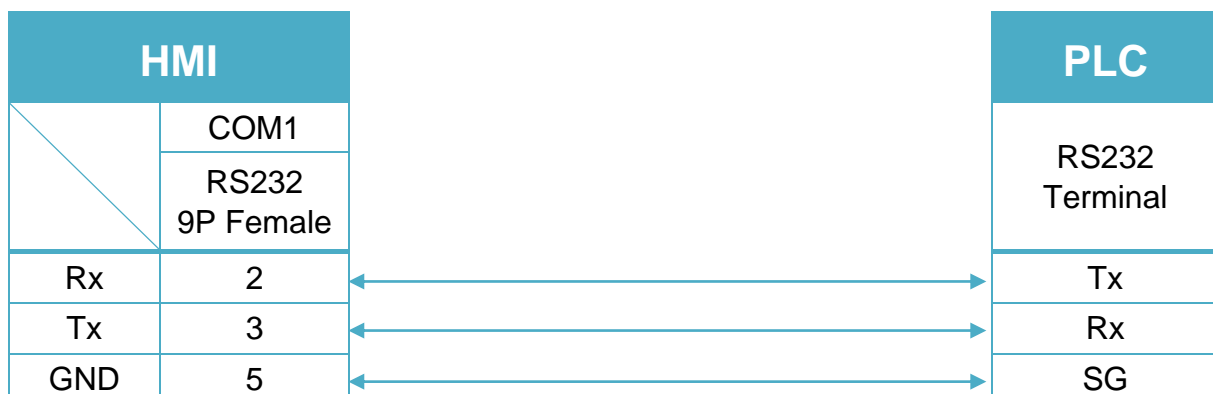
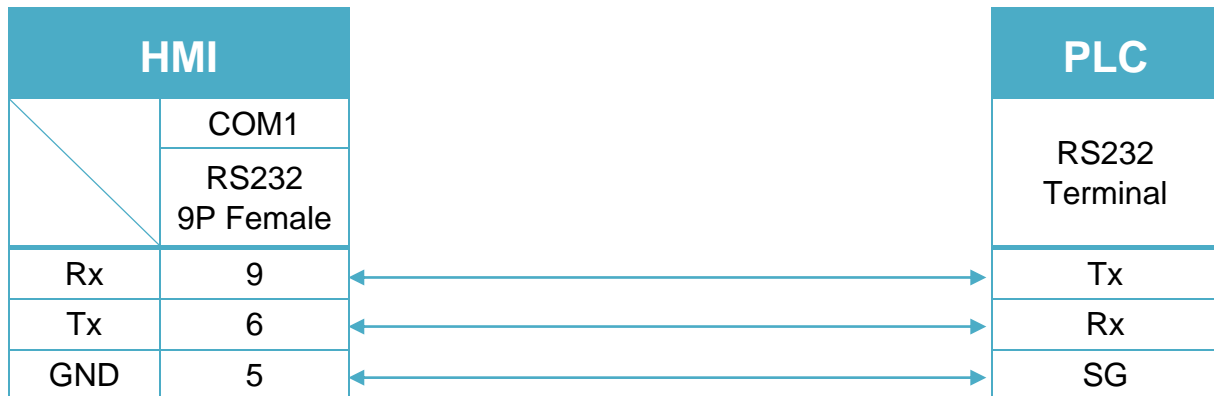


Diagram 3

MT-iE *MT8050iE / MT8053iE*

MT-iP *MT6051iP / MT8051iP / MT6071iP / MT8071iP*



RS485 2W Terminal (Diagram 4 ~ Diagram 9)

Diagram 4

cMT Series *cMT3151*

eMT Series *eMT3070 / eMT3105 / eMT3120 / eMT3150*

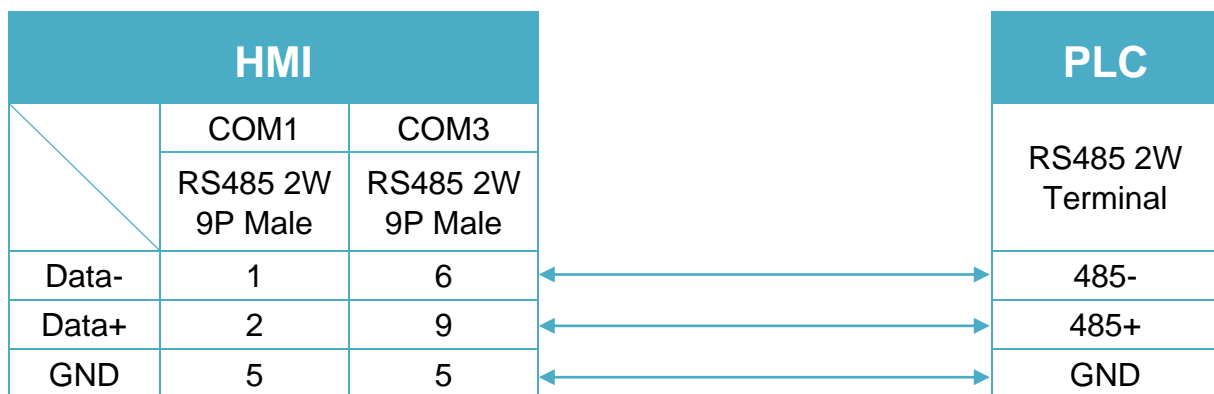


Diagram 5

cMT Series *cMT-SVR / cMT-G01 / cMT-G02 / cMT-HDM / cMT-FHD*

mTV *mTV*

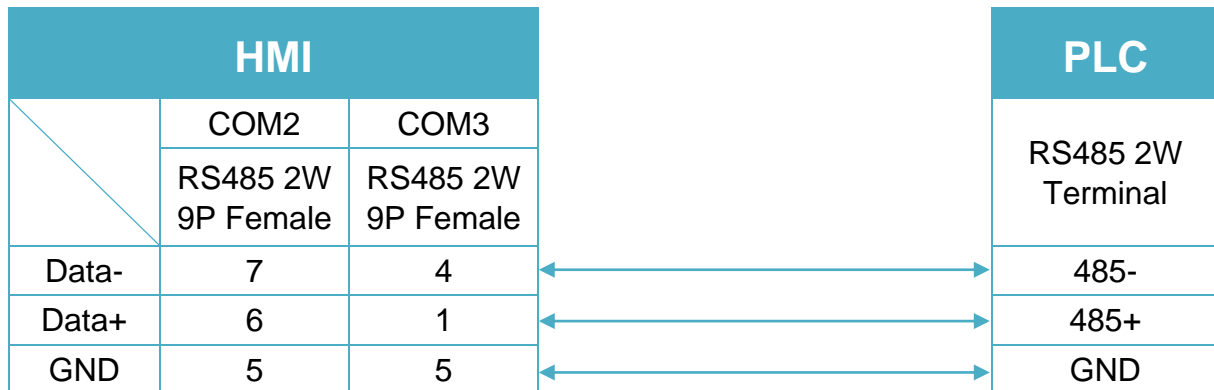


Diagram 6

MT-iE *MT8070iE / MT6070iE / MT8100iE / MT8121iE / MT8150iE*

MT-XE *MT8121XE / MT8150XE*

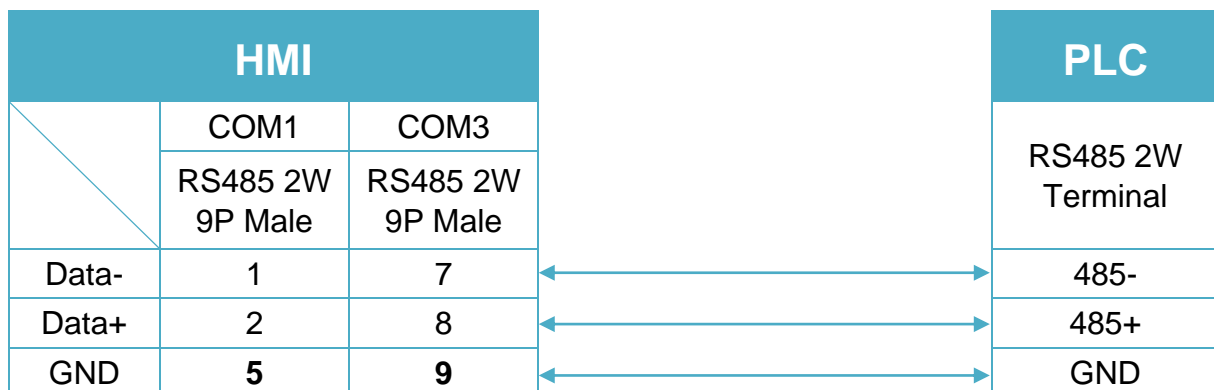


Diagram 7

cMT Series	<i>cMT3071 / cMT3072 / cMT3090 / cMT3103</i>
MT-iE	<i>MT8071iE / MT6071iE / MT8072iE / MT6072iE / MT8073iE / MT8101iE / MT8102iE / MT8103iE</i>
MT-XE	<i>MT8090XE / MT8092XE</i>
MT-iP	<i>MT6103iP / MT8102iP</i>

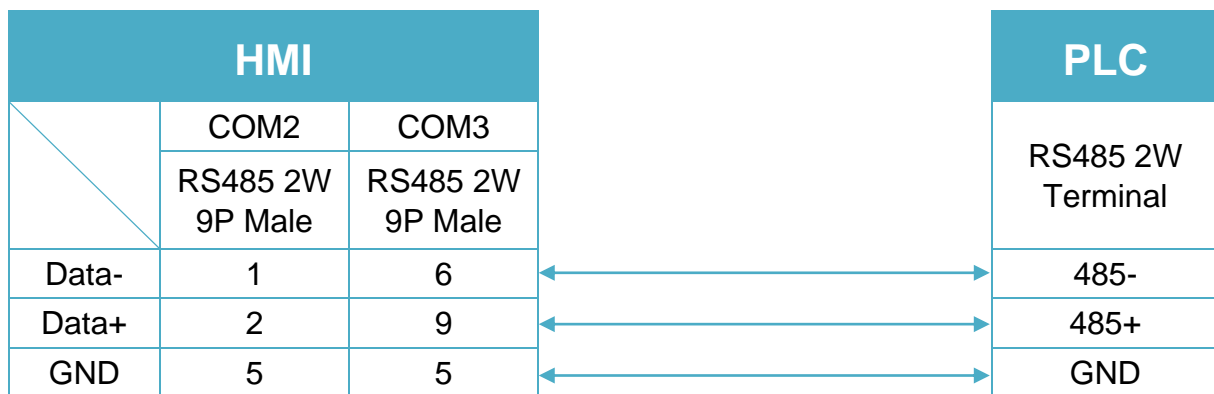


Diagram 8

MT-iE	<i>MT8050iE / MT8053iE</i>
MT-iP	<i>MT6051iP / MT8051iP</i>

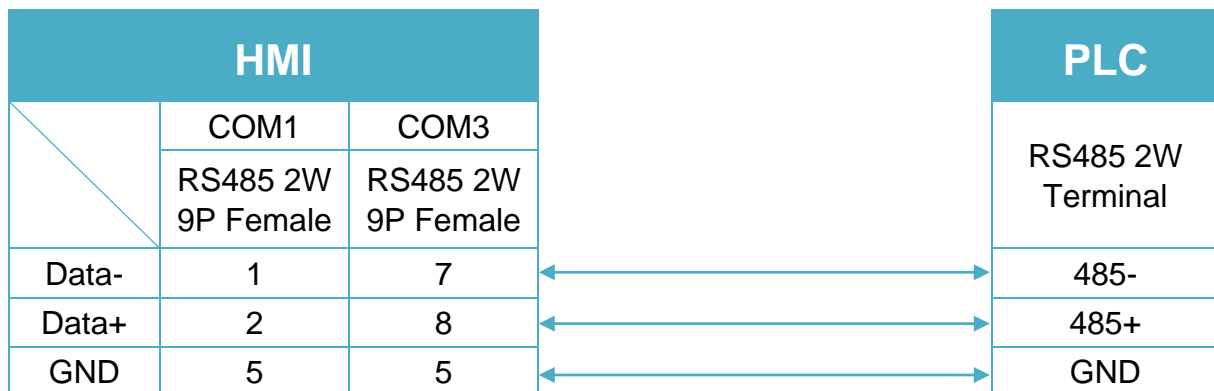


Diagram 9
MT-iP
MT6071iP / MT8071iP
