

Mitsubishi AJ71 (Format 4)

Supported Series: Mitsubishi A series PLC with AJ71C24 communication module using the Computer Link protocol.

Website: <http://www.mitsubishi-automation.com>

HMI Setting:

| Parameters | Recommended | Options | Notes |
|---------------------|----------------------------|-----------------|-------|
| PLC type | Mitsubishi AJ71 (Format 4) | | |
| PLC I/F | RS485 4W | RS485 4W, RS232 | |
| Baud rate | 19200 | 9600, 19200 | |
| Data bits | 8 | 8 | |
| Parity | Even | Even, Odd, None | |
| Stop bits | 1 | 1 | |
| PLC sta. no. | 0 | | |

PLC Setting:

| | |
|----------------------------|---|
| Communication mode | Computer Link protocol 9600, Even, 8, 1 (default) |
| Mode setting switch | Format 4 |
| Parity check | Enable |
| Sum check | Enable |

Device Address:

| Bit/Word | Device type | Format | Range | Memo |
|----------|-------------|--------|-----------|----------------------|
| B | X | HHHH | 0 ~ ffff | Input Bits |
| B | Y | HHHH | 0 ~ ffff | Output Bits |
| B | M | DDDDD | 0 ~ 65535 | Internal Relays |
| B | T | DDDDD | 0 ~ 65535 | |
| B | C | DDDDD | 0 ~ 65535 | |
| B | B | HHHH | 0 ~ ffff | |
| B | F | DDDDD | 0 ~ 65535 | |
| W | TV | DDDDD | 0 ~ 65535 | Timer Preset Value |
| W | CV | DDDDD | 0 ~ 65535 | Counter Preset Value |
| W | D | DDDDD | 0 ~ 65535 | Data Registers |
| W | W | HHHH | 0 ~ ffff | |
| W | R | DDDDD | 0 ~ 65535 | |

Wiring Diagram:

AJ71C24 RS422 Terminal (Diagram 1 ~ Diagram 4)

Diagram 1

| | |
|-------------------|--|
| cMT Series | <i>cMT3151</i> |
| eMT Series | <i>eMT3070 / eMT3105 / eMT3120 / eMT3150</i> |
| MT-iE | <i>MT8070iE / MT6070iE / MT8100iE / MT8121iE / MT8150iE</i> |
| MT-XE | <i>MT8121XE / MT8150XE</i> |

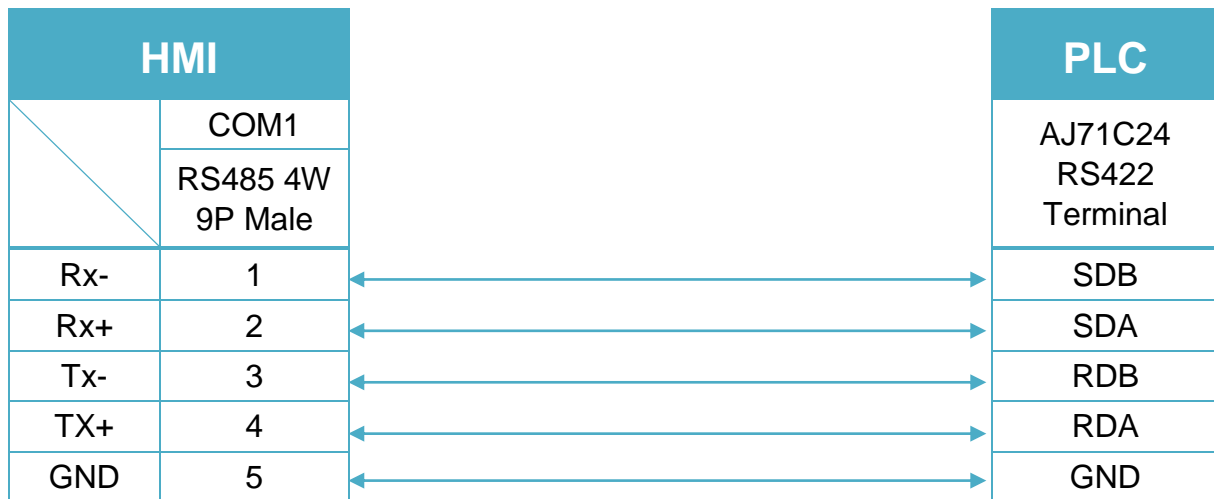


Diagram 2

cMT Series *cMT-SVR / cMT-G01 / cMT-G02 / cMT-HDM / cMT-FHD*

mTV *mTV*

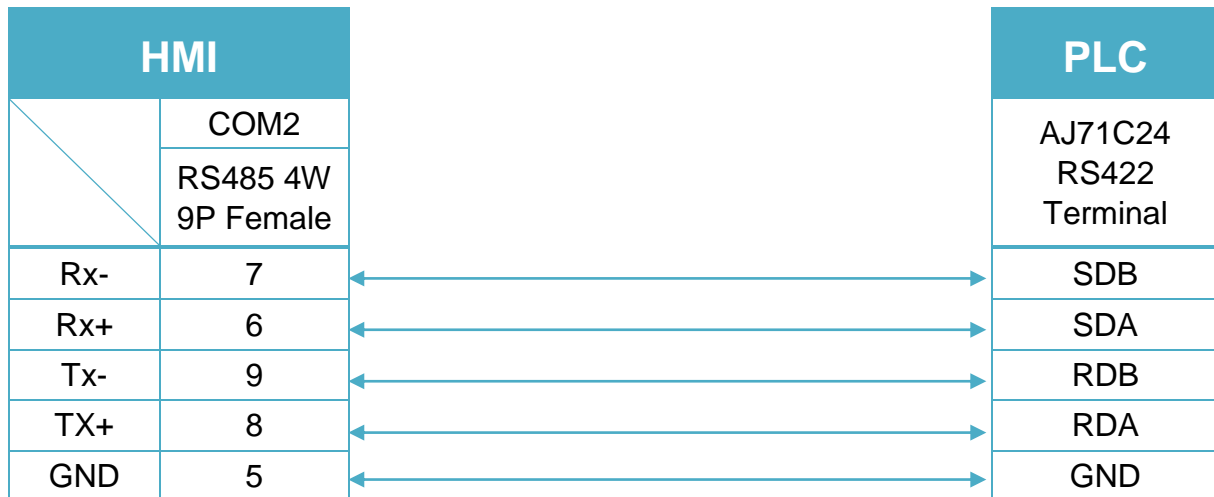


Diagram 3

cMT Series *cMT3071 / cMT3072 / cMT3090 / cMT3103*

MT-iE *MT8071iE / MT6071iE / MT8072iE / MT6072iE / MT8073iE /
MT8101iE / MT8102iE / MT8103iE*

MT-XE *MT8090XE / MT8092XE*

MT-iP *MT6071iP / MT8071iP / MT6103iP / MT8102iP*

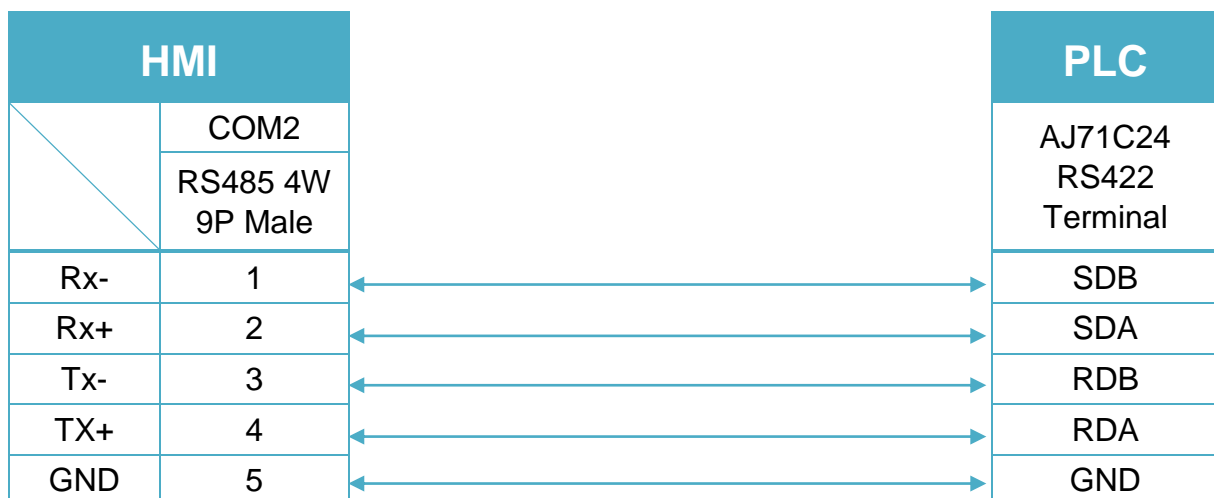
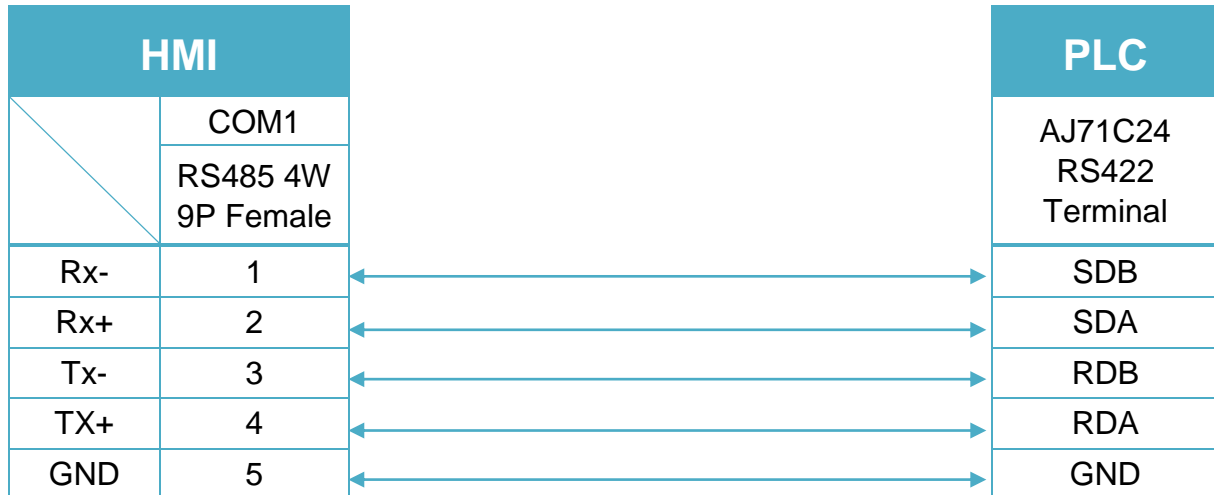


Diagram 4
MT-iE *MT8050iE / MT8053iE*
MT-iP *MT6051iP / MT8051iP*


RS232 (Diagram 5 ~ Diagram 8)

Diagram 5

cMT Series *cMT3071 / cMT3072 / cMT3090 / cMT3103 / cMT3151*

eMT Series *eMT3070 / eMT3105 / eMT3120 / eMT3150*

MT-iE *MT8073iE / MT8102iE*

MT-XE *MT8092XE*

MT-iP *MT6103iP / MT8102iP*

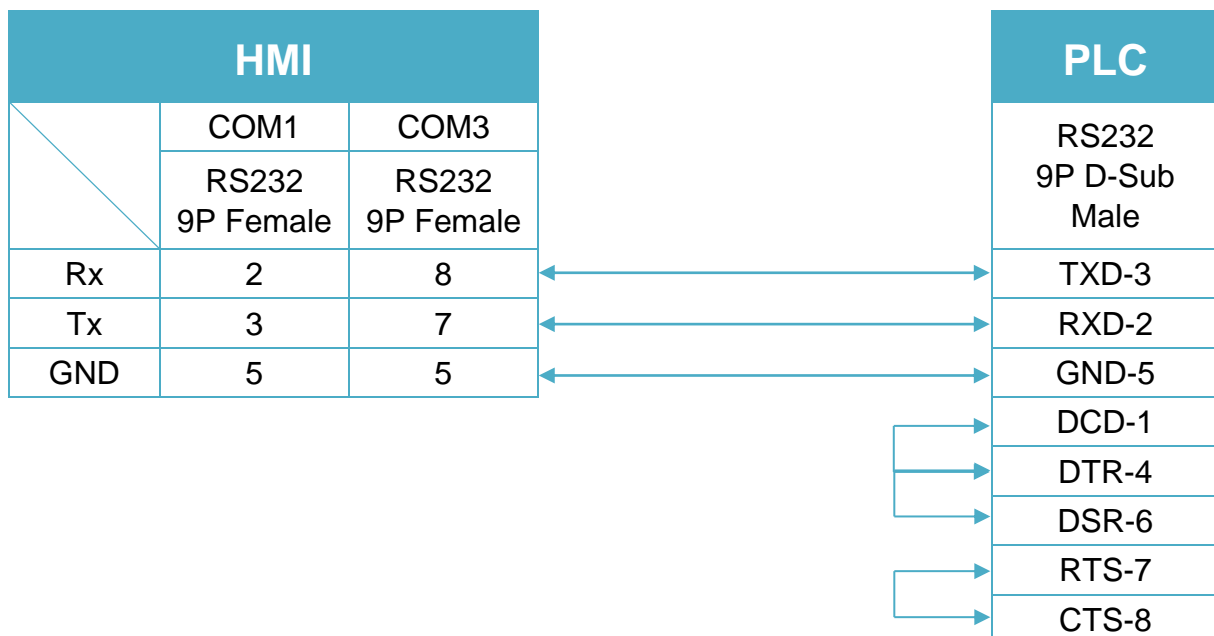


Diagram 6

| | |
|-------------------|--|
| cMT Series | <i>cMT-SVR / cMT-G01 / cMT-G02 / cMT-HDM / cMT-FHD</i> |
| mTV | <i>mTV</i> |
| MT-iE | <i>MT8070iE / MT6070iE / MT8100iE / MT8121iE / MT8150iE / MT8071iE / MT6071iE / MT8072iE / MT6072iE / MT8073iE / MT8101iE / MT8102iE / MT8103iE</i> |
| MT-XE | <i>MT8121XE / MT8150XE / MT8090XE</i> |

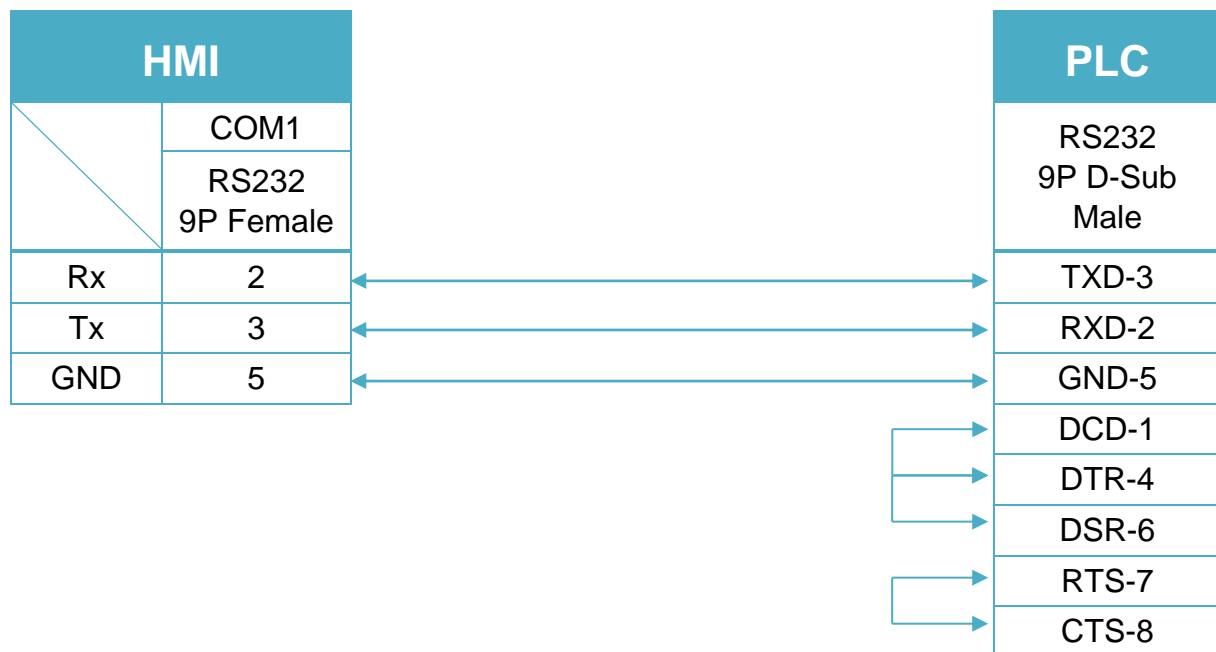


Diagram 7

MT-iE *MT8050iE / MT8053iE*

MT-iP *MT6051iP / MT8051iP / MT6071iP / MT8071iP*

