

Panasonic FP/KW

Supported Series: NAIS (Matsushita) FP/KW series include FP-X, FP-XH, FP-Σ, FP0, FP1, FP2, FP2SH, FP10SH

Website: <http://pewa.panasonic.com/>

HMI Setting:

Parameters	Recommended	Options	Notes
PLC type	Panasonic FP/KW		
PLC I/F	RS232	RS232/RS485	
Baud rate	9600	9600, 19200, 38400,	
Data bits	8	7 or 8	
Parity	Odd	Even, Odd, None	
Stop bits	1	1 or 2	
PLC sta. no.	1	1-32	Must match the PLC port setting. FP3 must set to 0.

*Support communications between HMI and PLC in pass-through mode

*Set LW-9903 to 2 to enhance the speed of download/upload PLC program in pass-through mode

Device Address:

Bit/Word	Device type	Format	Range	Memo
B	X	DDDDh	0 ~ 9999f	Input (X)
B	Y	DDDDh	0 ~ 9999f	Output (Y)
B	R	DDDDh	0 ~ 9999f	Internal Relay (R)
B	L	DDDD	0 ~ 9999	Link Relay (L)
B	L_Bit	DDDDh	0 ~ 9999f	
B	T	DDDD	0 ~ 9999	Timer (T)
B	C	DDDD	0 ~ 9999	Counter (C)
W	SV	DDDD	0 ~ 9999	Timer/Counter Set Value
W	EV	DDDDD	0 ~ 65535	Timer/Counter Elapse Value
W	DT	DDDDDD	0 ~ 262143	Data Register (DT)
W	LD	DDDD	0 ~ 8447	Link Register (LD)
W	WX	DDDD	0 ~ 9999	Input (WX) (read only)
W	WY	DDDD	0 ~ 9999	Output (WY)
W	WR	DDDD	0 ~ 9999	Internal Relay (WR)

Bit/Word	Device type	Format	Range	Memo
W	WL	DDDD	0 ~ 9999	Link Relay (WL)
W	FL	DDDDD	0 ~ 99999	File Register (FL)

Wiring Diagram:

The following is the view from the soldering point of a cable.

FP0, FP2, FP2SH, FPM CPU : 9P D-Sub to 5P Mini-DIN (Diagram 1 ~ Diagram 3)

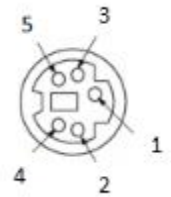


Diagram 1

cMT Series *cMT3071 / cMT3072 / cMT3090 / cMT3103 / cMT3151*

eMT Series *eMT3070/ eMT3105 / eMT3120 / eMT3150*

MT-iE *MT8073iE / MT8102iE*

MT-XE *MT8092XE*

MT-iP *MT6103iP/ MT8102iP*

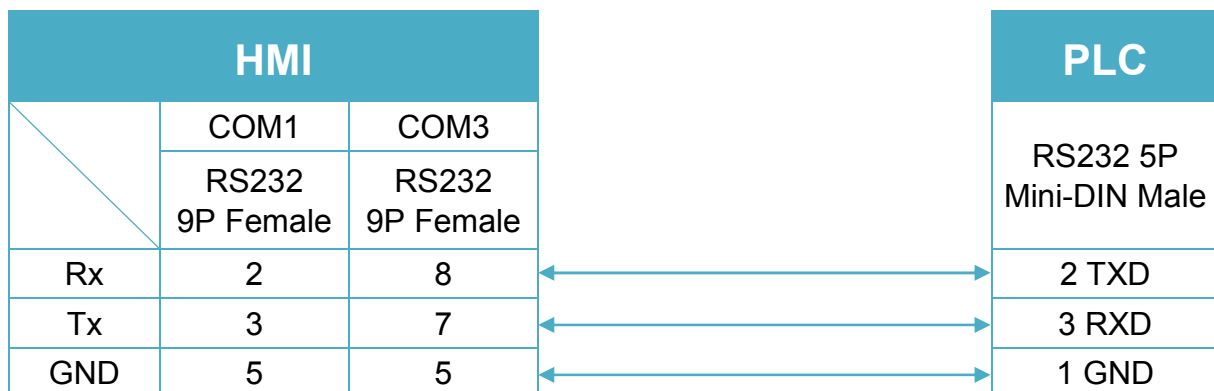


Diagram 2

cMT Series *cMT-SVR / cMT-G01 / cMT-G02 / cMT-HDM / cMT-FHD*

mTV *mTV*

MT-iE *MT8070iE / MT6070iE / MT8100iE / MT8121iE / MT8150iE /
MT8071iE / MT6071iE / MT8072iE / MT6072iE / MT8073iE /
MT8101iE / MT8102iE / MT8103iE*

MT-XE *MT8121XE / MT8150XE / MT8090XE*



Diagram 3

MT-iE *MT8050iE / MT8053iE*

MT-iP *MT6051iP / MT8051iP / MT6071iP / MT8071iP*



FP0 CPU : 9P D-Sub to 3P Terminal (Diagram 4 ~ Diagram 6)

Diagram 4

cMT Series ***cMT3071 / cMT3072 / cMT3090 / cMT3103 / cMT3151***

eMT Series ***eMT3070/ eMT3105 / eMT3120 / eMT3150***

MT-iE ***MT8073iE / MT8102iE***

MT-XE ***MT8092XE***

MT-iP ***MT6103iP/ MT8102iP***

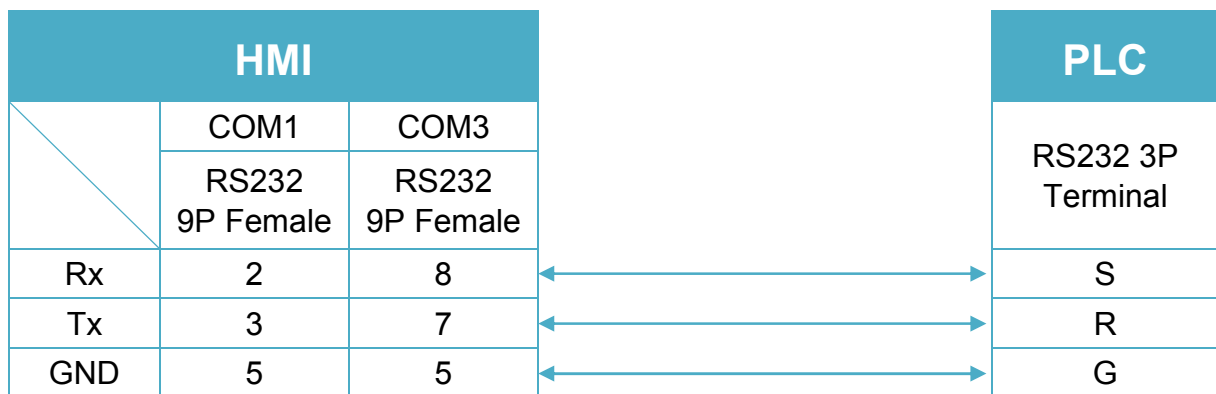


Diagram 5

cMT Series *cMT-SVR / cMT-G01 / cMT-G02 / cMT-HDM / cMT-FHD*

mTV *mTV*

MT-iE *MT8070iE / MT6070iE / MT8100iE / MT8121iE / MT8150iE /
MT8071iE / MT6071iE / MT8072iE / MT6072iE / MT8073iE /
MT8101iE / MT8102iE / MT8103iE*

MT-XE *MT8121XE / MT8150XE / MT8090XE*

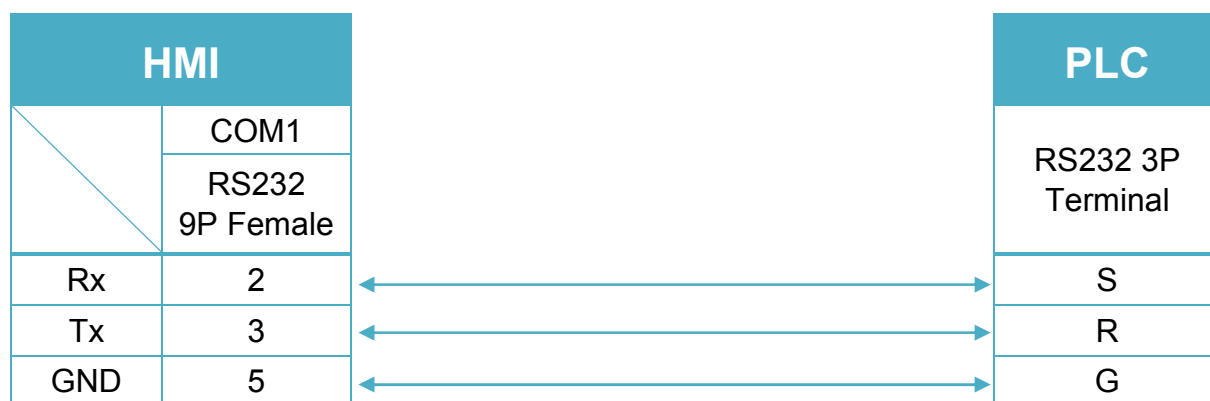
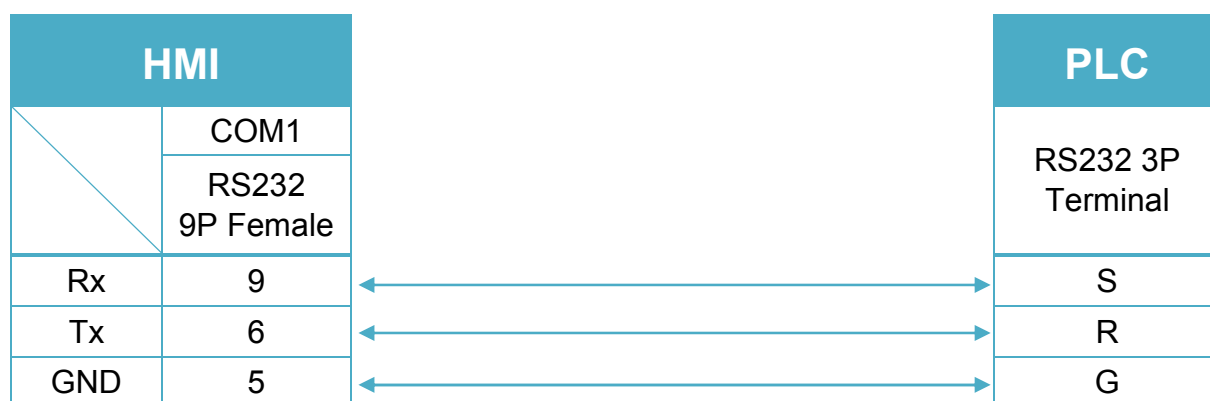


Diagram 6

MT-iE *MT8050iE / MT8053iE*

MT-iP *MT6051iP / MT8051iP / MT6071iP / MT8071iP*



RS232 9P D-Sub Male (Diagram 7 ~ Diagram 9)

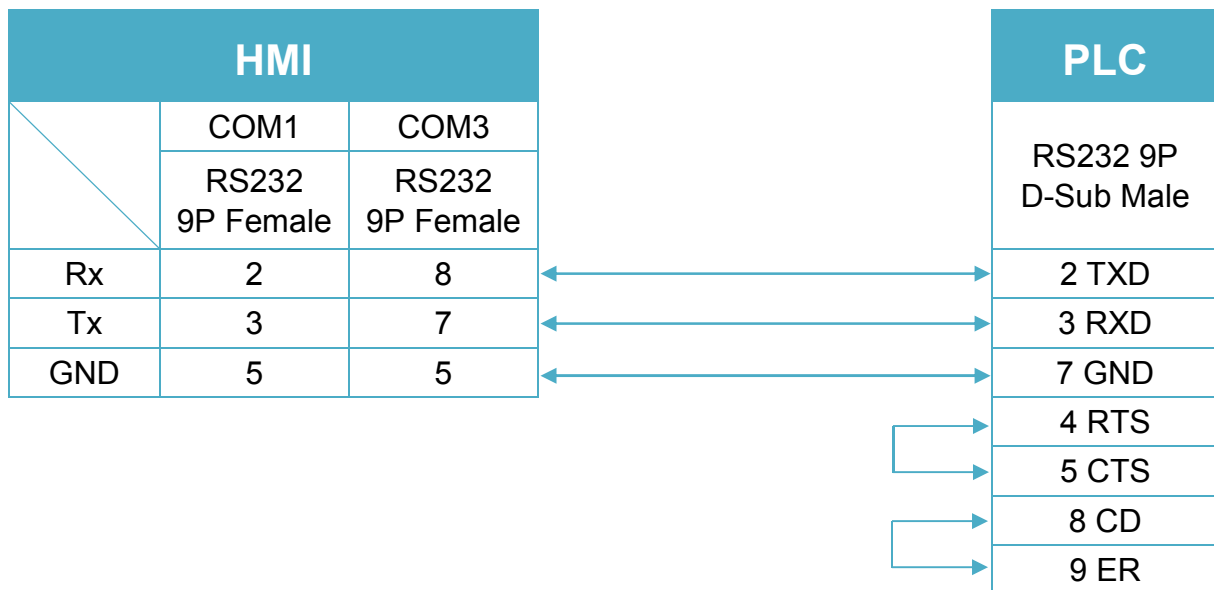
Diagram 7
cMT Series *cMT3071 / cMT3072 / cMT3090 / cMT3103 /cMT3151*
eMT Series *eMT3070/ eMT3105 / eMT3120 / eMT3150*
MT-iE *MT8073iE / MT8102iE*
MT-XE *MT8092XE*
MT-iP *MT6103iP/ MT8102iP*


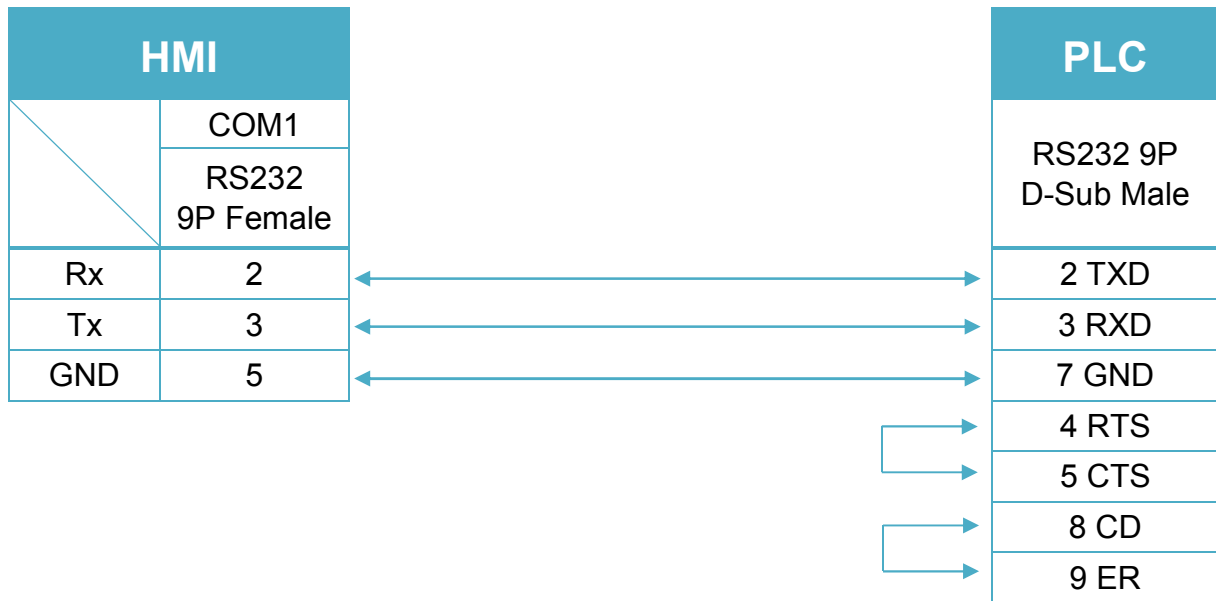
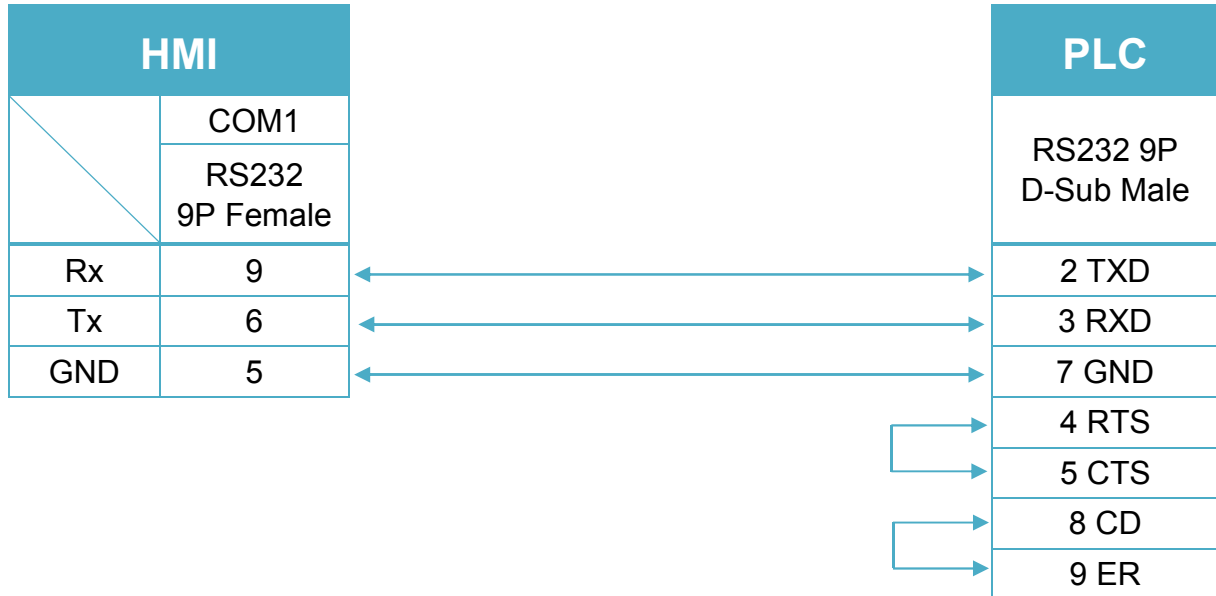
Diagram 8
cMT Series *cMT-SVR / cMT-G01 / cMT-G02 / cMT-HDM / cMT-FHD*
mTV *mTV*
MT-iE *MT8070iE / MT6070iE / MT8100iE / MT8121iE / MT8150iE /
MT8071iE / MT6071iE / MT8072iE / MT6072iE / MT8073iE /
MT8101iE / MT8102iE / MT8103iE*
MT-XE *MT8121XE / MT8150XE / MT8090XE*


Diagram 9**MT-iE** *MT8050iE / MT8053iE***MT-iP** *MT6051iP / MT8051iP / MT6071iP / MT8071iP*

The following is the view from the soldering point of a cable.

FP1 CPU : 9P D-Sub to 8P MiniDIN (Diagram 10 ~ Diagram 13)



Diagram 10

cMT Series	<i>cMT3151</i>
eMT Series	<i>eMT3070 / eMT3105 / eMT3120 / eMT3150</i>
MT-iE	<i>MT8070iE / MT6070iE / MT8100iE / MT8121iE / MT8150iE</i>
MT-XE	<i>MT8121XE / MT8150XE</i>

HMI			PLC
	COM1		RS485 4W 8P Hirose Female socket
	RS485 4W 9P Male		
Rx-	1	←→	2 TXDA
Rx+	2	←→	5 TXDB
Tx-	3	←→	3 RXDA
Tx+	4	←→	6 RXDB
GND	5	←→	1 GND

Diagram 11

cMT Series *cMT-SVR/ cMT-G01 / cMT-G02 / cMT-HDM / cMT-FHD*

mTV *mTV*

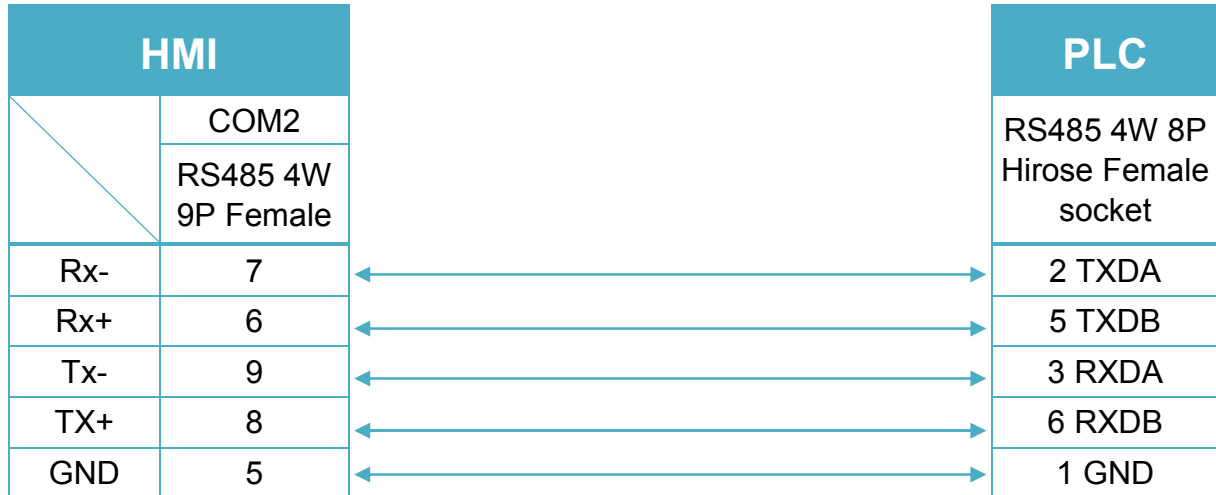


Diagram 12

cMT Series *cMT3071 / cMT3072 / cMT3090 / cMT3103*

MT-iE *MT8071iE / MT6071iE / MT8072iE / MT6072iE / MT8073iE / MT8101iE / MT8102iE / MT8103iE*

MT-XE *MT8090XE / MT8092XE*

MT-iP *MT6071iP / MT8071iP / MT6103iP/ MT8102iP*

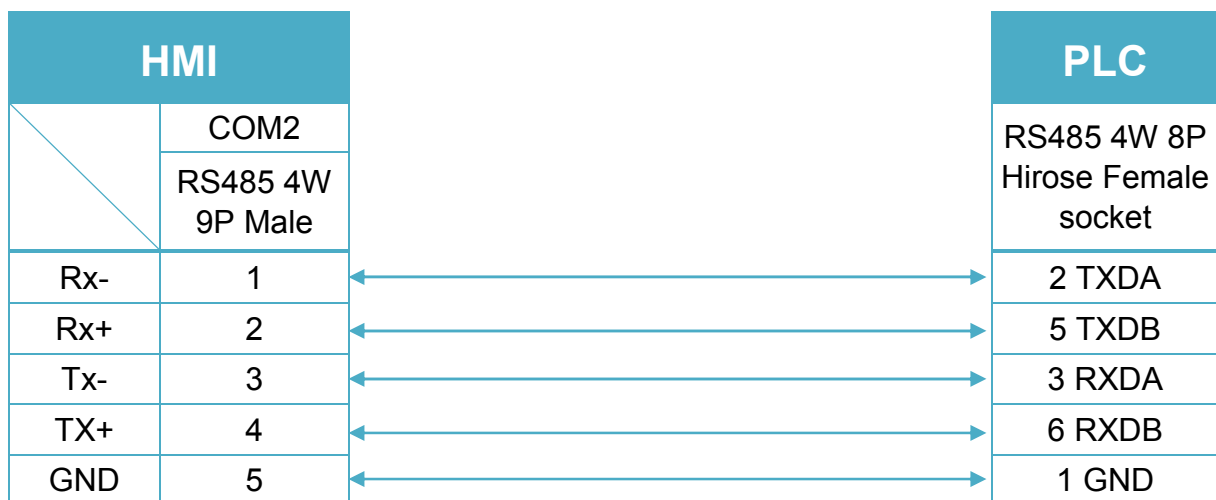
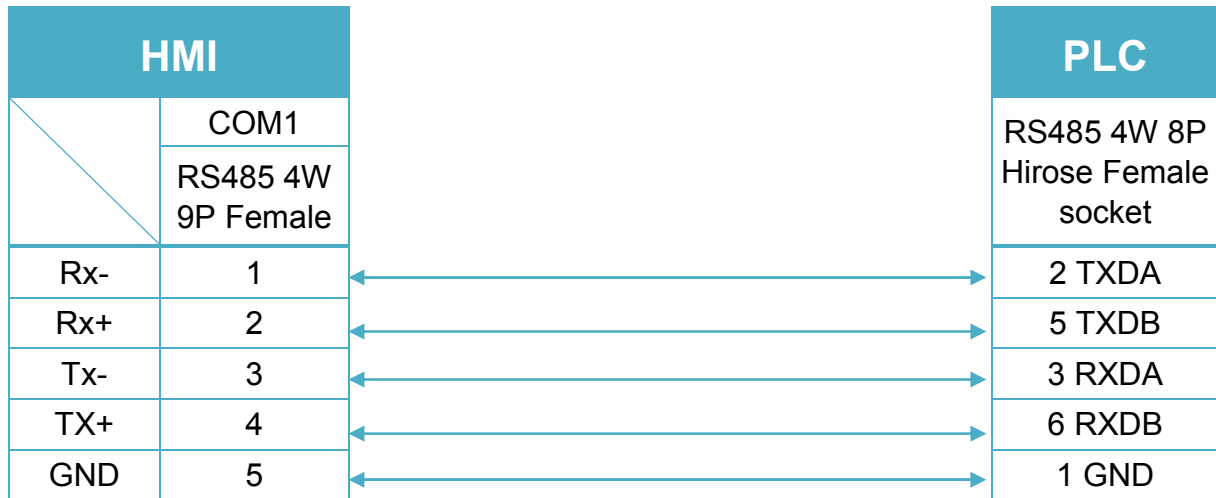


Diagram 13

MT-iE *MT8050iE / MT8053iE*

MT-iP *MT6051iP/ MT8051iP*



FP3 CPU : 9P D-Sub to 15P D-Sub (Diagram 14 ~ Diagram 17)

Diagram 14

cMT Series	<i>cMT3151</i>
eMT Series	<i>eMT3070 / eMT3105 / eMT3120 / eMT3150</i>
MT-iE	<i>MT8070iE / MT6070iE / MT8100iE / MT8121iE / MT8150iE</i>
MT-XE	<i>MT8121XE / MT8150XE</i>

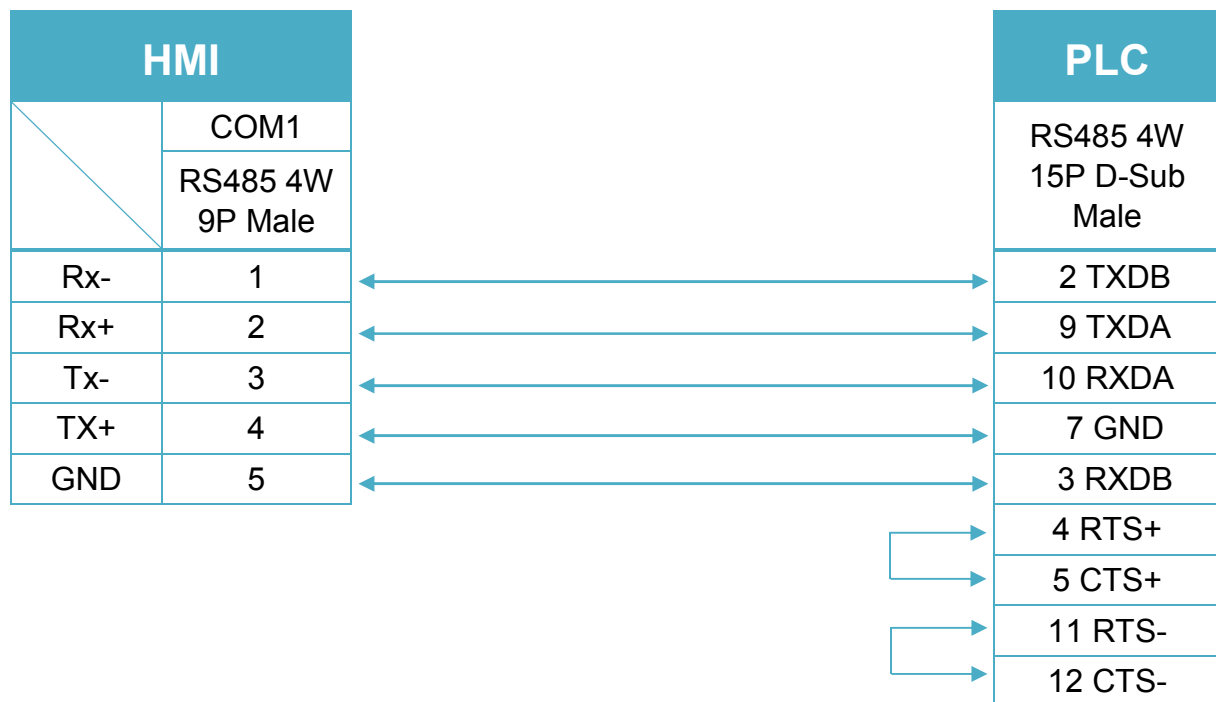


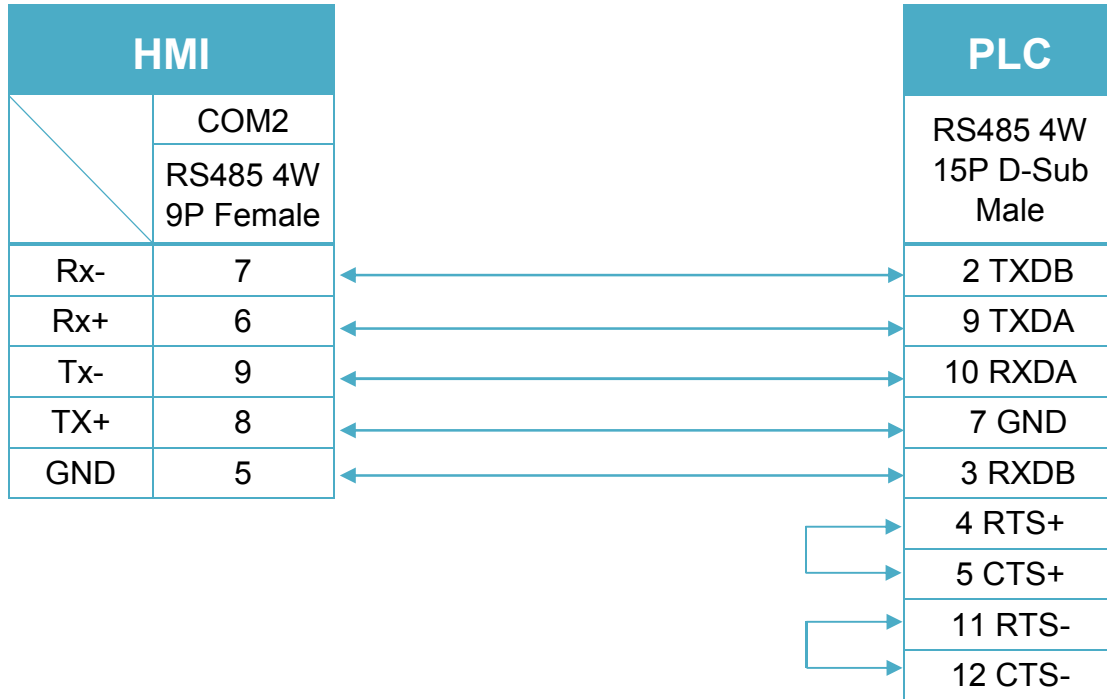
Diagram 15
cMT Series
cMT-SVR / cMT-G01 / cMT-G02 / cMT-HDM / cMT-FHD
mTV
mTV


Diagram 16

cMT Series	<i>cMT3071 / cMT3072 / cMT3090 / cMT3103</i>
MT-iE	<i>MT8071iE / MT6071iE / MT8072iE / MT6072iE / MT8073iE / MT8101iE / MT8102iE / MT8103iE</i>
MT-XE	<i>MT8090XE / MT8092XE</i>
MT-iP	<i>MT6071iP / MT8071iP / MT6103iP / MT8102iP</i>

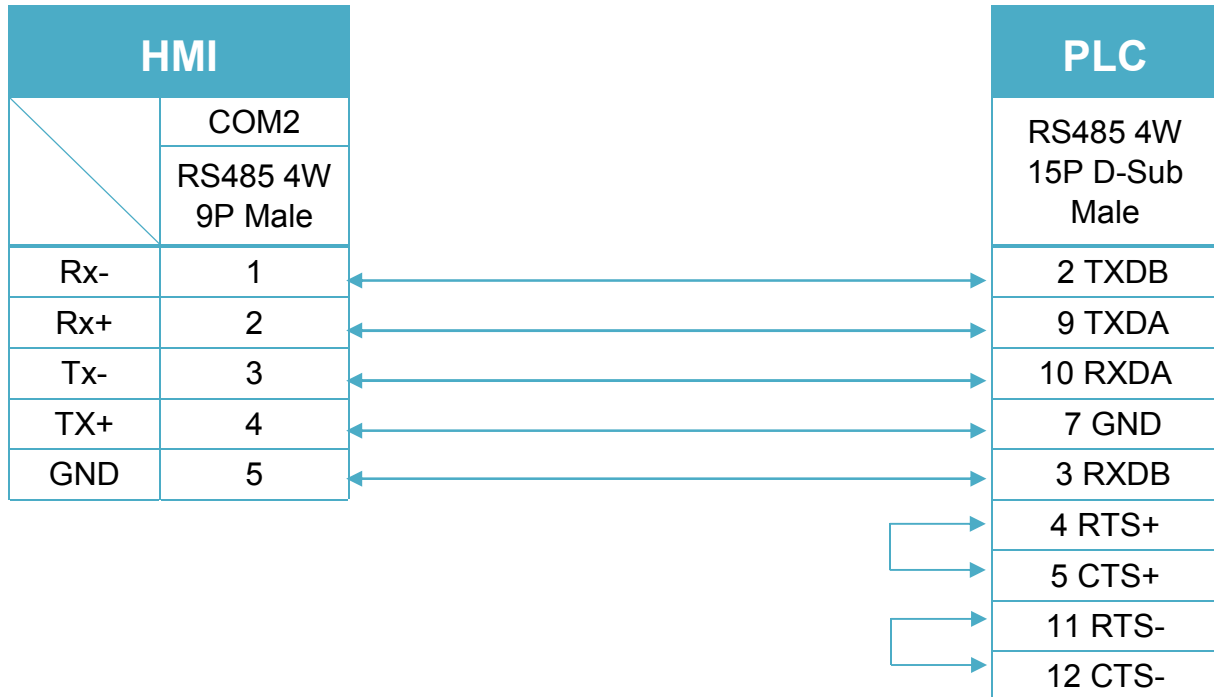


Diagram 17
MT-iE *MT8050iE/ MT8053iE*
MT-iP *MT6051iP/ MT8051iP*
