

SAIA PCD S-BUS Mode

Supported Series: SAIA PCD series S-Bus mode.

Website: <http://www.saia-burgess.com/>

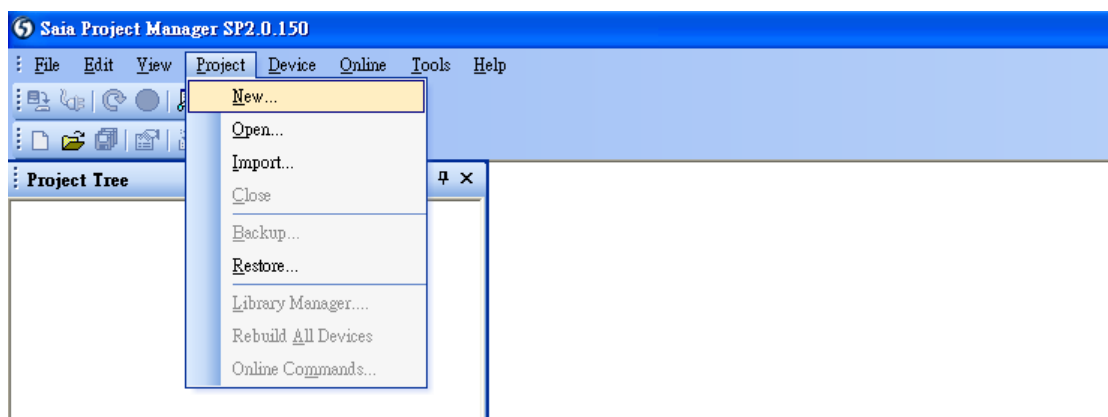
HMI Setting:

| Parameters | Recommended | Options | Notes |
|------------------|---------------------|--------------------|------------|
| PLC type | SAIA PCD S-BUS Mode | | PDS driver |
| PLC I/F | RS232 | RS232, RS485 | |
| Baud rate | 9600 | 9600, 19200, 38400 | |
| Data bits | 8 | 7,8 | |
| Parity | None | Even, Odd, None | |
| Stop bits | 1 | 1 | |
| PLC sta. | 0 | 0-255 | |

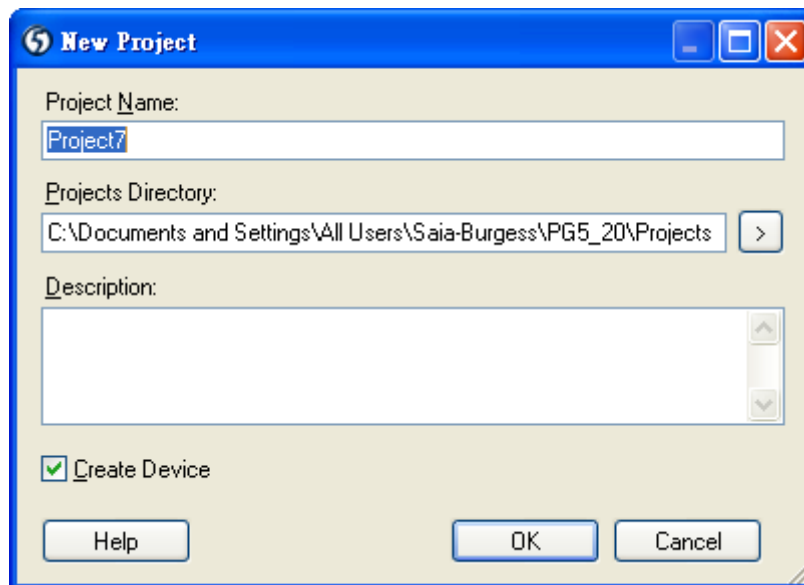
PLC Setting:

| | |
|---------------------------|--|
| Communication mode | 9600,N,8,1 (default) |
| RS232 | Port 0-Type: RS232 |
| RS485 2W | S-BUS Mode: Data(S2), Port 1-Type: RS485 |

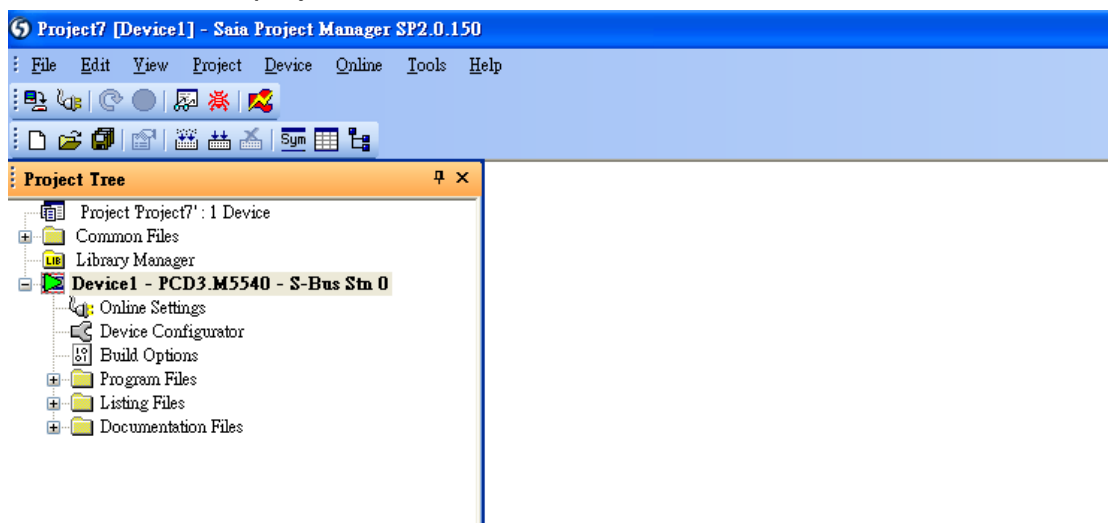
1. Open Saia Project Manager SP2.0.150 and create a new project.



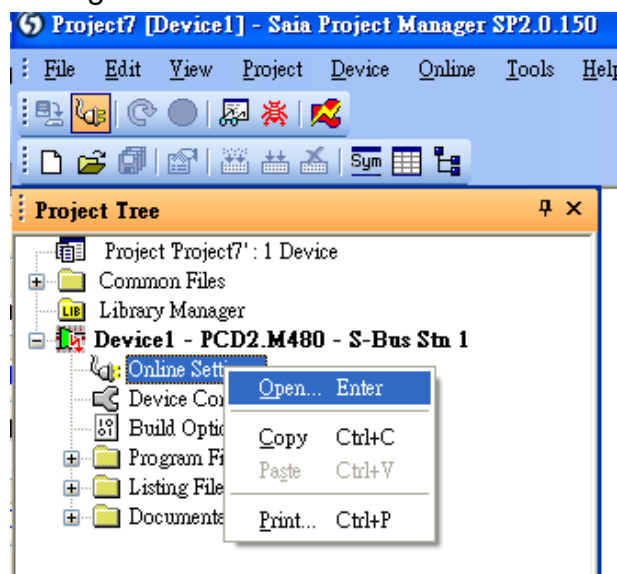
2. Give a project name.



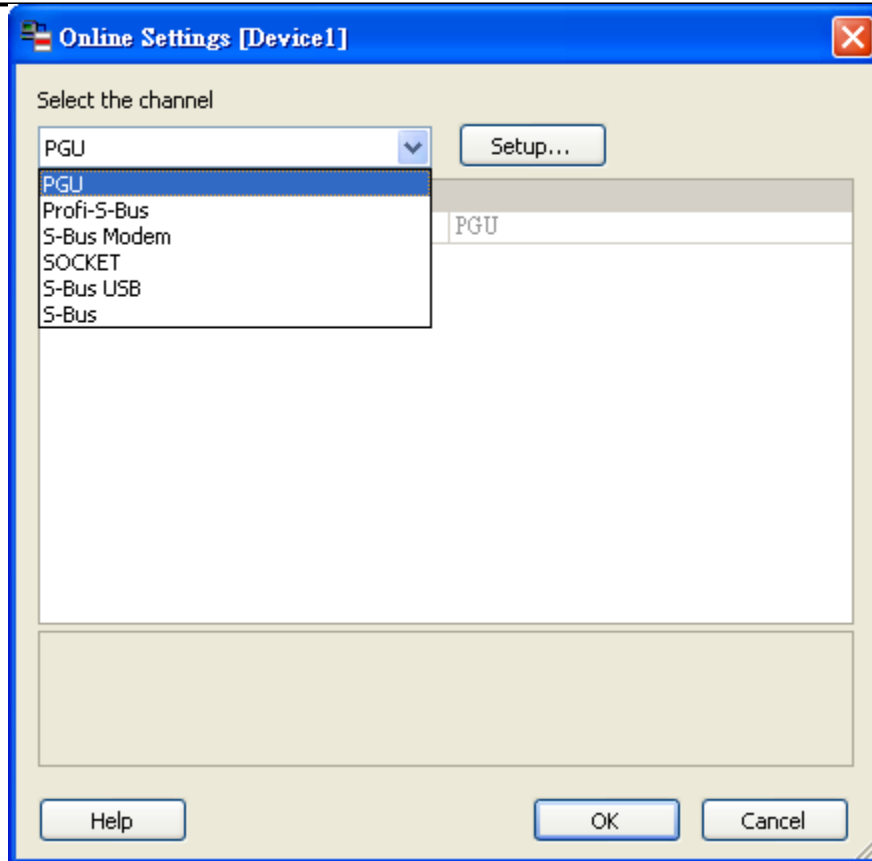
3. Create a new project as below.



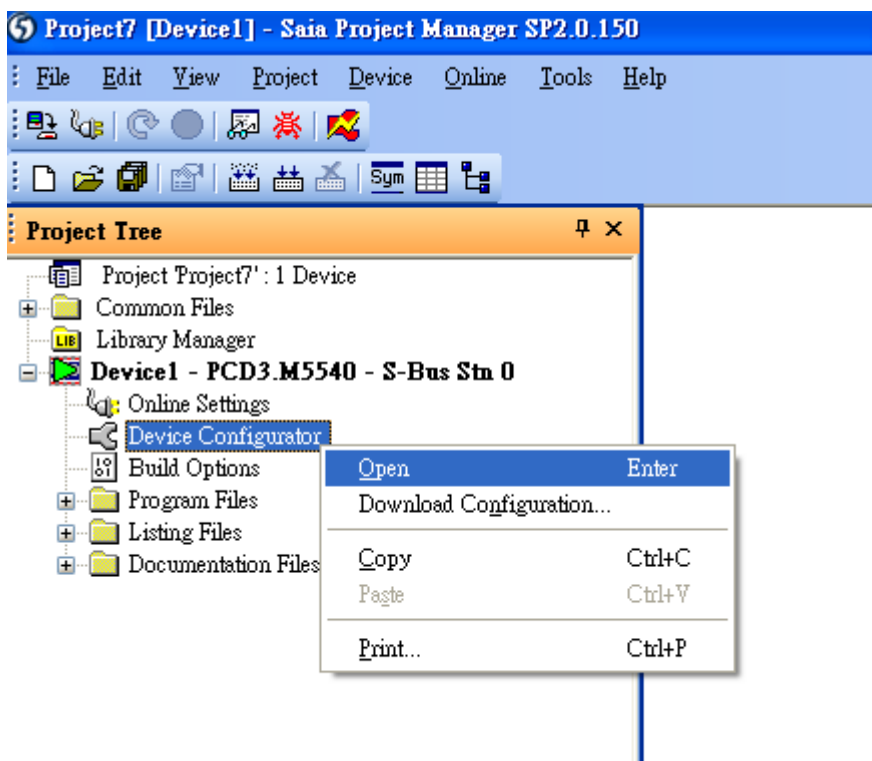
4. Go to "Online Setting".



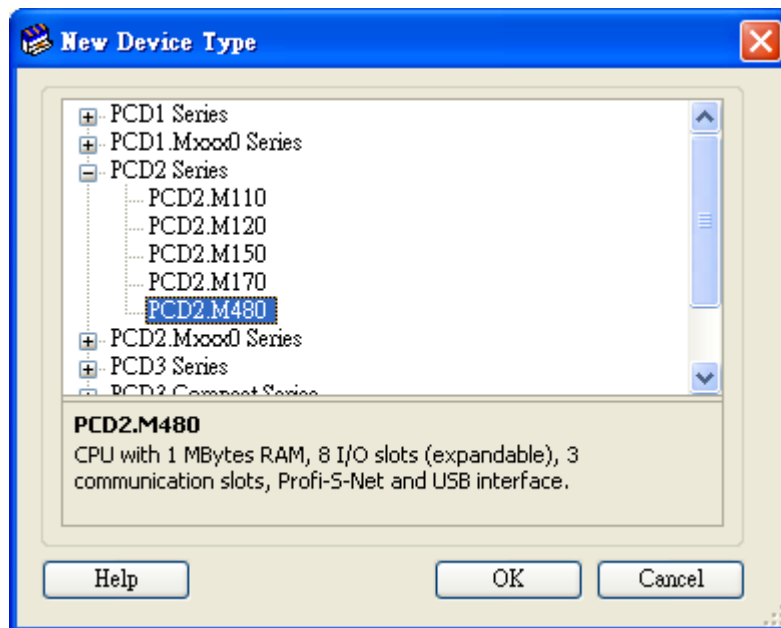
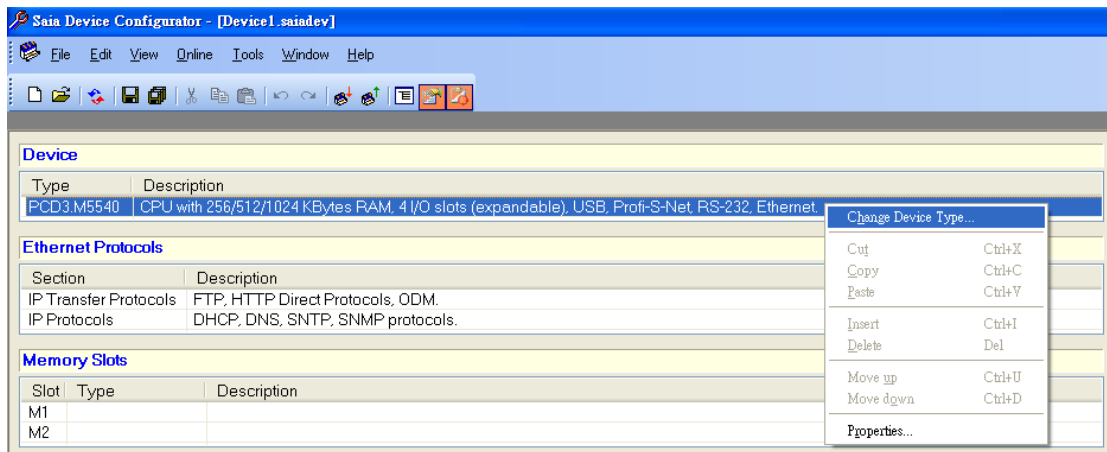
5. Select "PGU".



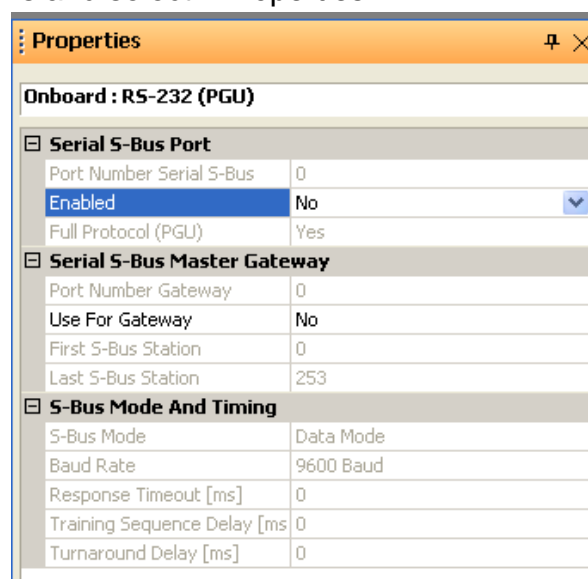
- Go to "Device Configurator".



- Click "Change Device Type" to select your PLC model.



8. Select RS232 (PGU) in Type and then right click mouse on Onboard Communications and select " Properties".



9. Select "Yes" in Series S-Bus Port: Enabled.

Properties

Onboard : RS-232 (PGU)

Serial S-Bus Port

| | |
|--------------------------|------------|
| Port Number Serial S-Bus | 0 |
| Enabled | Yes |
| Full Protocol (PGU) | Yes |

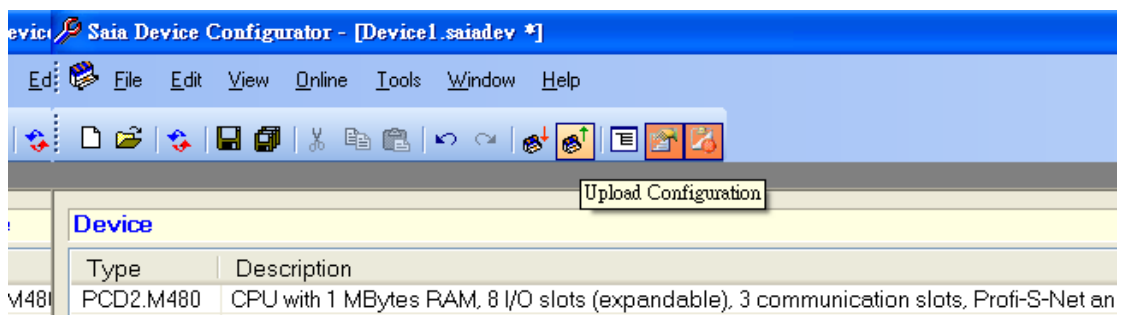
Serial S-Bus Master Gateway

| | |
|---------------------|-----|
| Port Number Gateway | 0 |
| Use For Gateway | No |
| First S-Bus Station | 0 |
| Last S-Bus Station | 253 |

S-Bus Mode And Timing

| | |
|------------------------------|-----------|
| S-Bus Mode | Data Mode |
| Baud Rate | 9600 Baud |
| Response Timeout [ms] | 0 |
| Training Sequence Delay [ms] | 0 |
| Turnaround Delay [ms] | 0 |

10. Set parameters in S-Bus Mode and Timing then upload to PLC.



Upload Configuration

Device configuration file name :

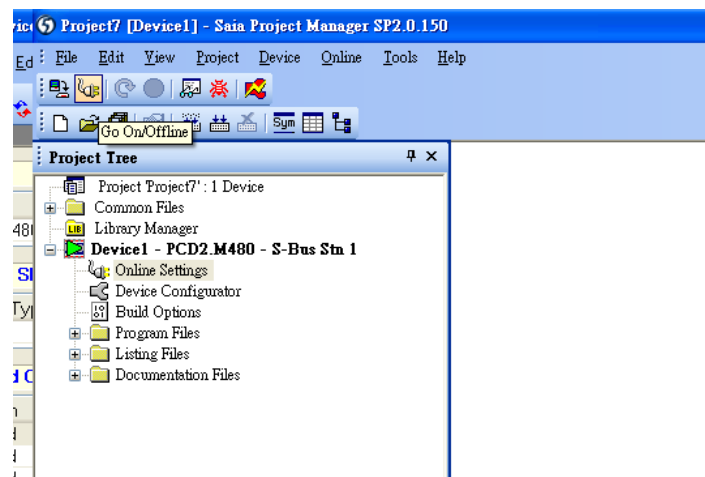
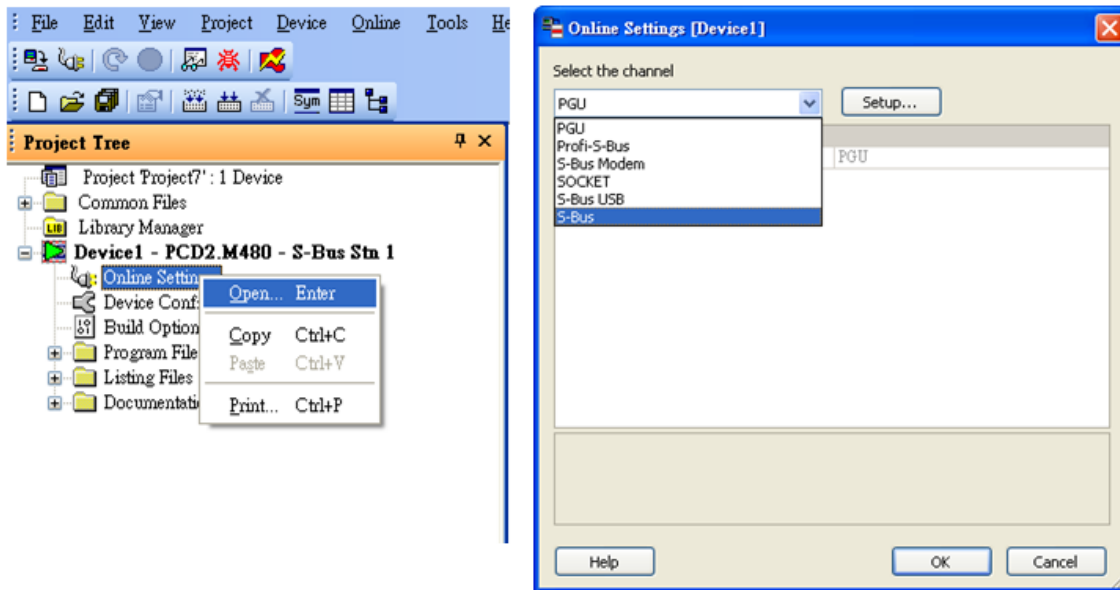
C:\Documents and Settings\All Users\Saia-Burgess\PG5_20\Projects\Project7\Device1\Device1.s

Upload on :

PGU

Help Upload Cancel

11. Go to Online Settings >> Open to select S-Bus for finishing the PLC settings.



Device Address:

| Bit/Word | Device type | Format | Range | Memo |
|----------|-------------|-----------|---------------|----------------------------|
| B | Flag | DDDD | 0 ~ 8191 | |
| B | Output | DDDD | 0 ~ 1023 | |
| B | Input | DDDD | 0 ~ 1023 | |
| B | Reg_Bit | DDDDdd | 0 ~ 1638331 | dd: Bit no. (00~31) |
| DW | Register | DDDDD | 0 ~ 16383 | |
| DW | Counter | DDDD | 0 ~ 1599 | |
| DW | Timer | DDDD | 0 ~ 1599 | |
| DW | Reg_Float | DDDDD | 0 ~ 16383 | support single float point |
| DW | DBn | DDDDDDDDD | 0 ~ 536016383 | |

Wiring Diagram:

SAIA PCD PGU Port RS232 9P D-Sub (Diagram 1 ~ Diagram 3)

Diagram 1

| | |
|-------------------|---|
| cMT Series | <i>cMT3071 / cMT3072 / cMT3090 / cMT3103 / cMT3151</i> |
| eMT Series | <i>eMT3070 / eMT3105 / eMT3120 / eMT3150</i> |
| MT-iE | <i>MT8073iE / MT8102iE</i> |
| MT-XE | <i>MT8092XE</i> |
| MT-iP | <i>MT6103iP / MT8102iP</i> |

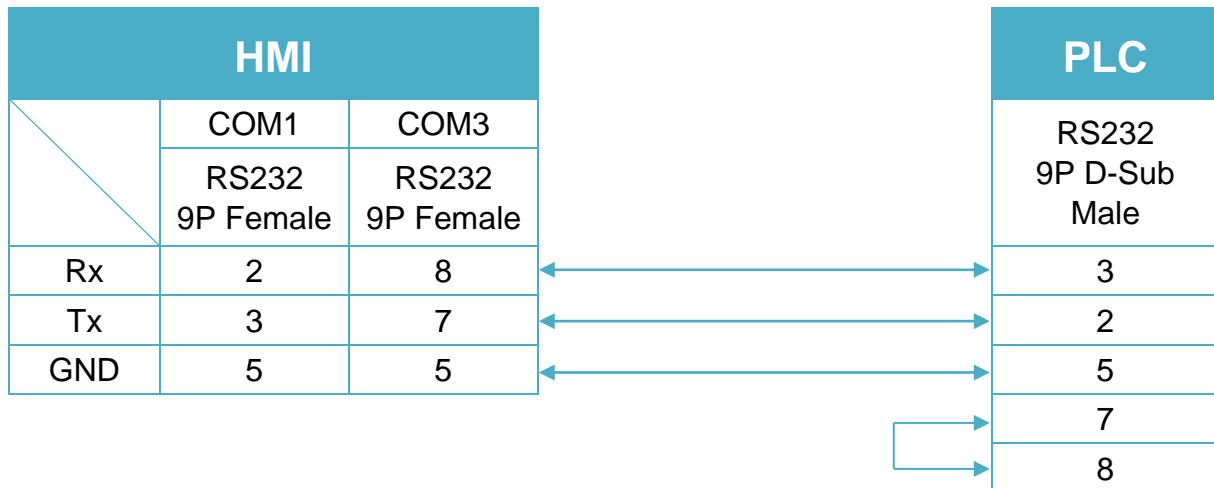


Diagram 2

| | |
|-------------------|--|
| cMT Series | <i>cMT-SVR / cMT-G01 / cMT-G02 / cMT-HDM / cMT-FHD</i> |
| mTV | <i>mTV</i> |
| MT-iE | <i>MT8070iE / MT6070iE / MT8100iE / MT8121iE / MT8150iE / MT8071iE / MT6071iE / MT8072iE / MT6072iE / MT8073iE / MT8101iE / MT8102iE / MT8103iE</i> |
| MT-XE | <i>MT8121XE / MT8150XE / MT8090XE</i> |

SAIA PCD1 Port #1 (Port #0) Terminal (Diagram 4 ~ Diagram 9)

Diagram 4

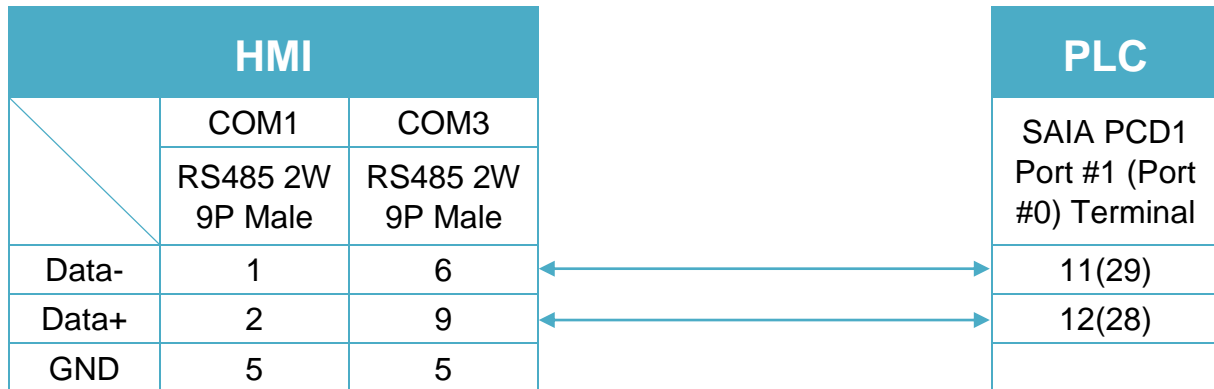
cMT Series *cMT3151*
eMT Series *eMT3070/ eMT3105 / eMT3120 / eMT3150*


Diagram 5

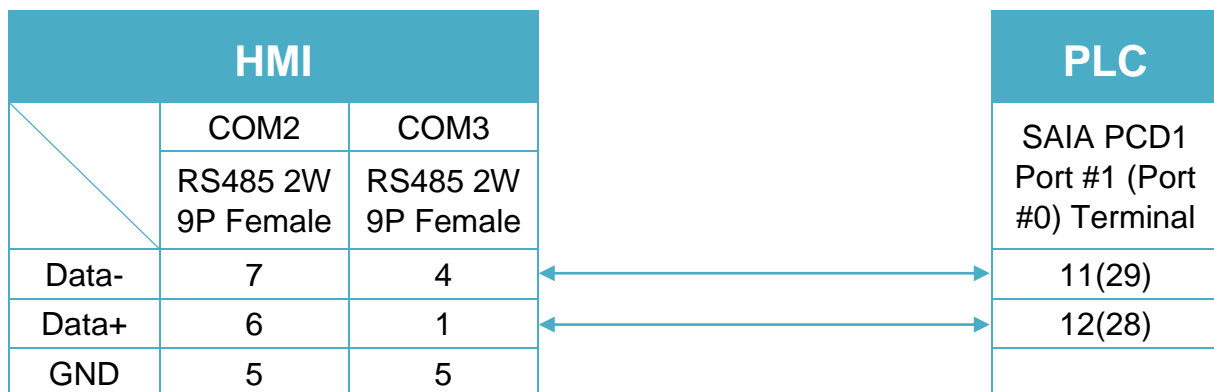
cMT Series *cMT-SVR/ cMT-G01 / cMT-G02 / cMT-HDM / cMT-FHD*
mTV *mTV*


Diagram 6

MT-iE *MT8070iE / MT6070iE / MT8100iE / MT8121iE / MT8150iE*

MT-XE *MT8121XE / MT8150XE*

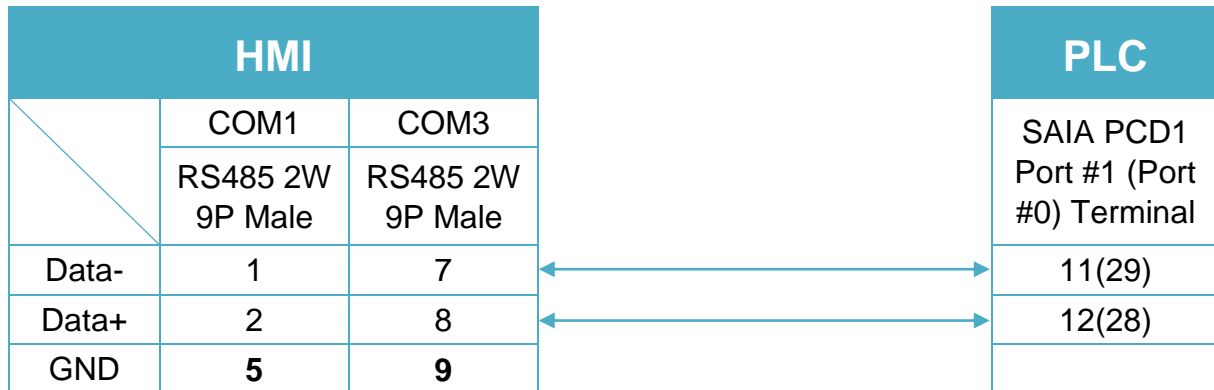


Diagram 7

cMT Series *cMT3071 / cMT3072 / cMT3090 / cMT3103*

MT-iE *MT8071iE / MT6071iE / MT8072iE / MT6072iE / MT8073iE /
MT8101iE / MT8102iE / MT8103iE*

MT-XE *MT8090XE / MT8092XE*

MT-iP *MT6103iP / MT8102iP*

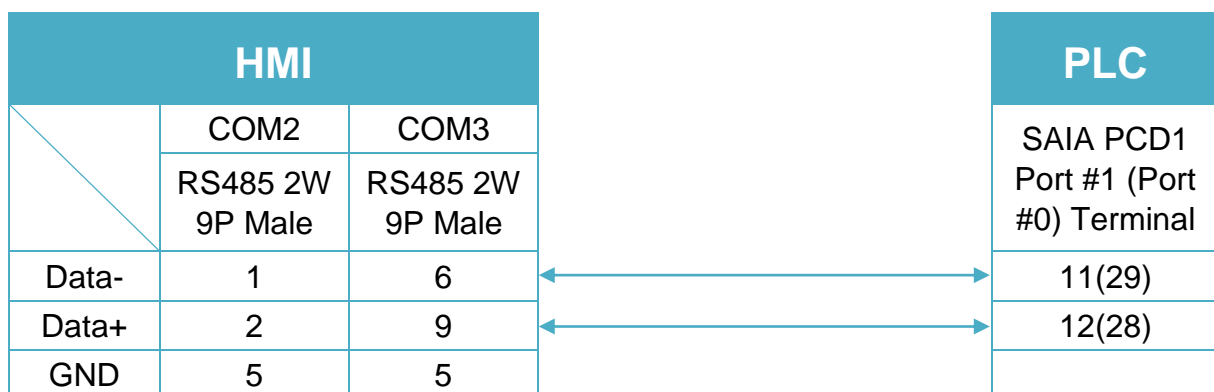


Diagram 8

MT-iE *MT8050iE/ MT8053iE*

MT-iP *MT6051iP/ MT8051iP*

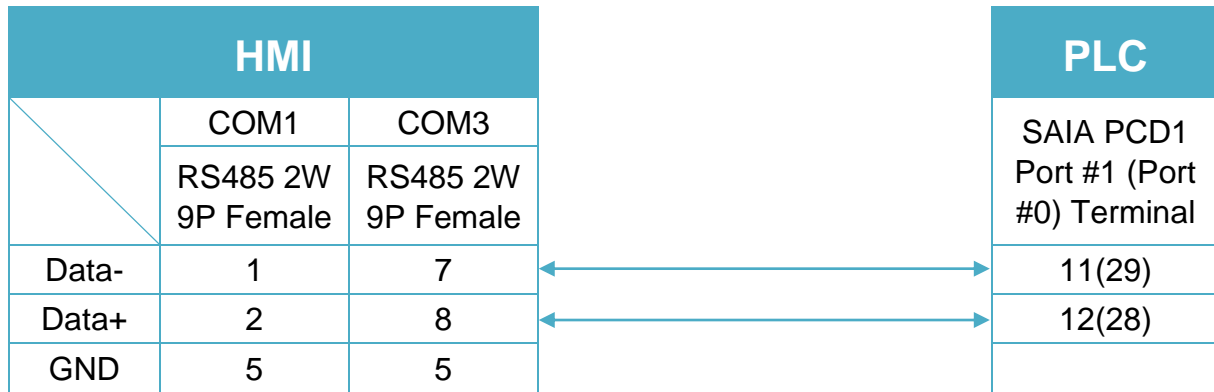


Diagram 9

MT-iP *MT6071iP / MT8071iP*

